Lesson Plan & Work-done Diary for AY: 2023-24, EVEN

Course with Code: Basic Electronics BBEE203				Faculty: Dr. Shalini Hanok and Ms. Navya N			Semester & Section: A,B.	
Module	**Class No.**	**Date Planned (DD/MM)**	**Topics to be covered**	**TLP Planned**	**Date of Conduction (DD/MM)**	**Topics Covered**	**TLP Executed**	**Remarks if any deviation**
MODULE-1	1		Introduction To Basic Electronics	Chalk and Talk				
	2		**Semiconductor Diodes:** Introduction	Chalk and Talk				
	3		PN Junction diode, Characteristics	Chalk and Talk				
	4		Diode Approximations, DC Load Line analysis	Chalk and Talk				
	5		**Diode Applications:** Introduction, Half Wave Rectification	Chalk and Talk				
	8		Full Wave Rectification, Full Wave Rectifier Power Supply:	Chalk and Talk				
	9		Capacitor Filter Circuit, RC π Filter, **Zener Diodes:** Junction Breakdown	Chalk and Talk				
	10		Circuit Symbol and Package, Circuit Symbol and Package	Chalk and Talk				
	11		Equivalent Circuit, Zener Diode Voltage Regulator, Problems	Chalk and Talk				

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MODULE-2	12		**Boolean Algebra and Logic Circuits:** Introduction to Binary numbers.	Chalk and Talk				
	13		Number Base Conversion, octal & Hexa Decimal Numbers	Chalk and Talk				
	14		Complements, Basic definitions, Axiomatic Definition of Boolean Algebra	Chalk and Talk				
	15		, Basic Theorems and Properties of Boolean Algebra	Chalk and Talk				
	16		Boolean Functions, Canonical and Standard Forms.	Chalk and Talk				
	17		Other Logic Operations, Digital Logic Gates	Chalk and Talk				
	18		**Combinational logic**: Introduction, Design procedure, Adders- Half adder, Full adder	Chalk and Talk				

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MODULE-3	19		**Bipolar Junction Transistors:** Introduction BJT Voltages & Currents	Chalk and Talk				
	20		BJT Amplification, Common Base Characteristics,	Chalk and Talk				
	21		Common Emitter Characteristics.	Chalk and Talk				
	22		Common Collector Characteristics.	Chalk and Talk				
	23		BJT Biasing: Introduction	Chalk and Talk				
	24		DC Load line and Bias point	Chalk and Talk				
	25		**Field Effect Transistor:** Junction Field Effect Transistor.	Chalk and Talk				
	26		JFET Characteristics, MOSFETs: Enhancement MOSFETs,	Chalk and Talk				

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MODULE-4	27		**Operational Amplifiers**: Introduction, The Operational Amplifier, Block Diagram Representation of Typical Op-Amp, Schematic Symbol.	Chalk and Talk				
	28		Op-Amp parameters - Gain, input resistance, Output resistance.	Chalk and Talk				
	29		CMRR, Slew rate, Bandwidth, input offset voltage, Input bias Current and Input offset Current	Chalk and Talk				
	30		The Ideal Op-Amp, Equivalent Circuit of Op-Amp, Open Loop Op-Amp configurations, Differential Amplifier.	Chalk and Talk				
	31		Inverting & Non Inverting Amplifier **Op-Amp Applications:** Inverting Configuration	Chalk and Talk				
	32		Non-Inverting Configuration, Differential Configuration	Chalk and Talk				
	33		Voltage Follower, Integrator, Differentiator	Chalk and Talk				
	34		Problems	Chalk and Talk				
	35		Problems	Chalk and Talk				

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MODULE-5	36		**Introduction to Transducers:** Introduction, Resistive Transducers.	PPT				
	37		Inductive Transducers, Capacitive Transducers.	PPT				
	38		Thermal transducers, Optoelectronic transduce.	PPT				
	39		Piezoelectric transducers	PPT				
	40		**Communications:** Introduction to communication, Communication System, Modulation	PPT				

	Activity	Planned	Actual	Remarks
1	Theory Classes	40	40	
2	Assignments/Quizzes/ Self-study	2	2	
3	Tutorials/ Extra classes			
4	Internal Assessments	3	3	
5	ICT based Teaching (% of usage in Curriculum)			
Planning			**Execution**	
Faculty Signature:			**Faculty Signature:**	
HOD Signature:			**HOD Signature:**	